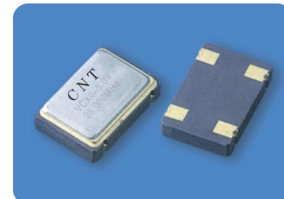




VOLTAGE CONTROLLED SMD CRYSTAL (VCXO) OSCILLATOR 7.0×5.0

FEATURES

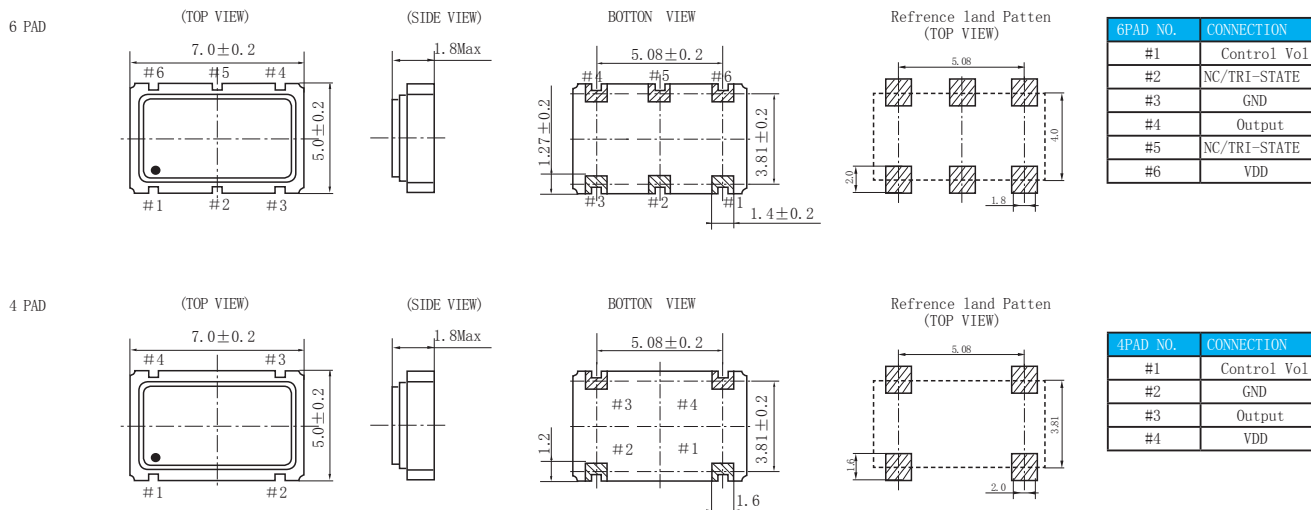
- Stability up to $\pm 5\text{ppm}$
- Control voltage range $\pm 200\text{ppm Max}$
- CMOS/TTL compatible output



SPECIFICATIONS

ITEM		DESCRIPTION	
Frequency range		1.000MHz~80.000MHz	
Pad Type		4Pads or 6Pad	
Supply Voltage (VDD)		5.0V DC $\pm 10\%$	3.3V DC $\pm 10\%$
Supply Current		$\leq 30\text{MHz}$ 30mA Max, $> 30\text{MHz}$ 45mA Max	
Operating Temperature Range		$-10^{\circ}\text{C} \sim +60^{\circ}\text{C}$, $-20^{\circ}\text{C} \sim +70^{\circ}\text{C}$, $-40^{\circ}\text{C} \sim +85^{\circ}\text{C}$ or specify	
Frequency Stability Over Operating Temp Characteristics		$\pm 30\text{ppm (STD)}$, $\pm 5\text{ppm}$ ~ $\pm 50\text{ppm}$ or specify	
Storage Temperature Range		$-55^{\circ}\text{C} \sim +125^{\circ}\text{C}$	
Frequency Control	Positive Transfer Characteristic		
	Pullability	$\pm 50\text{ppm}$, $\pm 100\text{ppm}$, $\pm 150\text{ppm}$, $\pm 200\text{ppm}$ MIN	
	Center Control Voltage	$V_c = 2.5\text{V DC}$	$V_c = 1.65\text{V DC}$
Output Character	Control Voltage	0.5V DC to 4.5V DC	0.3V DC to 3.0V DC
	"0" Level (Vol Max)	0.5V (10%VDD) Max	0.3V (10%VDD) Max
	"1" Level (Voh Min)	4.5V (90%VDD) Min	3.0V (90%VDD) Min
	Rise Time (Tr)	10%~90%VDD 5ns Max	
	Fall Time (Tf)	90%~10%VDD 5ns Max	
	Symmetry	40%~60% or 45%~55% Option (at 50%VDD)	
Start Up Time	15pf, 30pf, 50pf Max or specify		
	7~10ms Max		
Aging ($25^{\circ}\text{C} \pm 3^{\circ}\text{C}$)		$\pm 3\text{ppm/year}$	

OUTLINE DIMENSIONS (mm)



PART NUMBER

TIM-	VCS7050A	A	16.000000	± 50	± 10	$-20^{\circ}\text{C} \sim +70^{\circ}\text{C}$	5.0	N
Type	Molde Type	Frequency (MHz)	Control voltage range	Frequency stability	operating temperature range	Supply voltage (v)	E/D Control	
VCS7050A: 6PAD	A: CMOS/TTL	16.000000:	(ppm)	(ppm)		5.0: 5.0V DC	Y: TRI-STATE	
VCS7050B: 4PAD	B: CMOS C: TTL	16.000000MHz	± 50 ; $\pm 50\text{ppm}$	± 10 ; $\pm 10\text{ppm}$		3.3: 3.3V DC	N: N/C Control	